FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

SERIAL NO. ATTY. DOCKET NO. New CIR 108339-09058 APPLICANT

LIST OF REFERENCES CITED BY APPLICANT

Michael BOWES

November 15, 2000

FILING DATE

(Use several sheets if necessary)

			U.S. PAT	ENT DOCUMENTS			60
EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
Mm	AA	5,473,607	Dec. 5, 1995	Hausman et al.			,
	AB	5,787,084	Jul. 28, 1998	Hoang et al.			
	AC	5,423,015	Jun. 6, 1995	Chung			
	AD	5,568,477	Oct. 22, 1996	Galand et al.			
	AE	5,696,899	Dec. 9, 1997	Kalwitz			
	AF	5,790,539	Aug. 4, 1998	Chao et al.			
	AG	5,802,287	Sep. 1, 1998	Rostoker et al.			
	АН	5,825,772	Oct. 20, 1998	Dobbins et al.			
	Al	5,831,980	Nov. 3, 1998	Varma et al.			
	AJ	5,845,081	Dec. 1, 1998	Rangarajan et al.			
	AK	5,887,187	Mar. 23, 1999	Rostoker et al.			
	AL	5,892,922	Apr. 6, 1999	Lorenz			
	АМ	5,898,687	Apr. 27, 1999	Harriman et al.			
	AN	5,909,686	Jun. 1, 1999	Muller et al.			
	AO	5,918,074	Jun. 29, 1999	Wright et al.			
	АР	5,987,507	Nov. 16, 1999	Creedon et al.			
	ΔQ	5,940,596	Aug. 17, 1999	Rajan et al.			
V	AR	6,011,795	Jan. 4, 2000	Varghese et al.			

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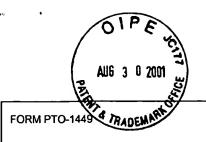
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	Sheet <u>1</u> of <u>3</u>
ATTY. DOCKET NO.	SERIAL NO.
108339-09058	09/712,130
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Michael J. BOWES	
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	AB	5,644,784	Jul. 1, 1997	Peek =			Mar. 3, 1995
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	BQ	EP0859492A2	Aug. 19, 1998	EPO					
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MM	CN	"Computer Networks," A.S. Tanenbaum, PRENTICE-HALL INT., USA, XP-002147300(1998), Sec. 5.2-Sec. 5.3, pages 309-320

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Sheet 1 of 1 ATTY, DOCKET NO. SERIAL NO. **FORM PTO-1449** U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE 108339-09058 09/712,130 **APPLICANT** REFERENCES CITED BY APPLICANT **BOWES** Jse several sheets if necessary) FILING DATE GROUP NOV 0 2 2001 November 15, 2000 2662 **U.S. PATENT DOCUMENTS** EXAMINER SUB-**DOCUMENT FILING** INITIAL DATE NAME **CLASS** CLASS DATE NO. AA ΑB AC ΑD ΑE AF **FOREIGN PATENT DOCUMENTS** DOCUMENT SUB-TRANSLATION YES NO PART. **CLASS** DATE COUNTRY CLASS NO. AG ΑH ΑI ΑJ ΑK AL OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.) Muller, S., "Architecture for a 10-Gigabit Ethernet Standard", IEEE 802.3 Higher Speed Study Group, MM June 1999, Interim Week Meeting, Online June 1, 1999, XP002178292 AM Lin B et al, "Synthesis of Hazard-Free Multi-Level Logic under Multiple-Input Changes from Binary Decision Diagrams", IEEE/ACM International Conference on Computer-Aided Design, Digest of MM AN Technical Papers (ICCAD), San Jose, November 6-10, 1994, Los Alamitos, pp 542-549, XP000529867 AO **EXAMINER DATE CONSIDERED**

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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE ATTY. DOCKET NO. SERIAL NO. **New CIP** 108339-09058

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Michael BOWES

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	AF	WO 99/00939	7 January 1999	WIPO					Ш
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			OTHER	DOCUMENTS			•	
V	AJ	WO 99/00944	7 January 1999	WIPO				
	Al	WO 99/00945	7 January 1999	WIPO		Ц		
	АН	WO 99/00938	7 January 1999	WIPO		Ц		
	AG	WO 99/00950	7 January 1999	WIPO		 Ц		
	AF	WO 99/00939	7 January 1999	WIPO		Ц		
	+	WO 33/00343	7 January 1555	WIIO	-	 \vdash		

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